UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/773,266	02/09/2004	Chi-Cheng Ju	3722-0176P	8567

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EXAMINER
HSU, JONI

ART UNIT	PAPER NUMBER
2628	

NOTIFICATION DATE	DELIVERY MODE
11/06/2007	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

mailroom@bskb.com

PTOL-90A (Rev. 04/07)

	Application No.	Applicant(s)
Office Action Summary	10/773,266	JU ET AL.
	Examiner	Art Unit	
	Joni Hsu	2628	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *31 August 2007*.

2a)☐ This action is **FINAL.** 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-4 and 6-15* is/are pending in the application.

　　4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-4 and 6-15* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

　　Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

　　Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

　　a)☐ All　b)☐ Some * c)☐ None of:

　　　1.☐ Certified copies of the priority documents have been received.

　　　2.☐ Certified copies of the priority documents have been received in Application No. _____ .

　　　3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

　　* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO/SB/08)
　　Paper No(s)/Mail Date _____ .

4)☐ Interview Summary (PTO-413)
　　Paper No(s)/Mail Date. _____ .

5)☐ Notice of Informal Patent Application

6)☐ Other: _____ .

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1.   A request for continued examination under 37 CFR 1.114, including the fee set forth in

37 CFR 1.17(e), was filed in this application after final rejection. Since this application is

eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e)

has been timely paid, the finality of the previous Office action has been withdrawn pursuant to

37 CFR 1.114. Applicant's submission filed on August 31, 2007 has been entered.

Response to Arguments

2.   Applicant's arguments with respect to claims 1-4 and 6-15 have been considered but are

moot in view of the new ground(s) of rejection.

3.   Applicant's arguments, see page 6, filed August 31, 2007, with respect to the rejection(s)

of claim(s) 1-4 and 6-15 under 35 U.S.C. 103(a) have been fully considered and are persuasive.

Therefore, the rejection has been withdrawn. However, upon further consideration, a new

ground(s) of rejection is made in view of Perrin (US005872577A).

4.   Applicant argues that neither McGuinness (US006104416A) nor Vinekar

(US005581310A) teach that at least one memory page only has a row with a plurality of memory

cells having a first memory second and a second memory section (page 6).

In reply, the Examiner agrees. However, new grounds of rejection are made in view of

Perrin.

Claim Rejections - 35 USC § 103

5.   The text of those sections of Title 35, U.S. Code 103(a) not included in this action can be

found in a prior Office action.

6.   Claims 1-4 and 6-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over

McGuinness (US006104416A) in view of Perrin (US005872577A).

7.   With regard to Claim 1, McGuinness describes a method of storing an array of digital

data into a memory (Col. 3, lines 14-16) having a plurality of memory pages, at least one

memory page which only has a row with a plurality of memory cells (*pixels in a row of one*

stripe are stored in a word, word address, Col. 8, lines 45-58), the method comprising the steps

of dividing the array of digital data into a plurality of block units (Col. 3, lines 16-18) each of the

block units having a plurality of odd rows and a plurality of even rows (Col. 11, line 51-Col. 12,

line 13), each of the odd rows and the even rows having at least one byte (*one byte of storage is*

required for each pixel, Col. 4, lines 47-48; *putting 16 pixels into each row*, Col. 11, lines 52-

54); storing subsequent odd rows of at least one of the block units into consecutive storage

locations in the first memory section (532) (Col. 11, lines 57-63), and storing subsequent even

rows of at least one of the block units into consecutive storage locations in the second memory

section (534) (Col. 11, line 65-Col. 12, line 13).

However, McGuinness does not specifically teach that at least one memory page has the first memory section and the second memory section. However, Perrin discloses at least one memory page which only has a row with a plurality of memory cells having a first memory section and a second memory section; and storing subsequent odd fields into consecutive storage locations in the first memory section, and storing subsequent even fields into consecutive storage locations in the second memory section (Col. 3, lines 1-9; Figure 3).

It would have been obvious to one of ordinary skill in the art at the time of invention by applicant to modify the device of McGuinness so that at least one memory page has the first memory section and the second memory section as suggested by Perrin because Perrin suggests that this organization allows for several types of access without any page jumps (Col. 1, lines 43-63), which increases accessing speed.

8.   With regard to Claim 2, McGuinness describes that the array of digital data comprises a picture in a video bit stream (Col. 4, line 64-Col. 5, line 9).

9.   With regard to Claim 3, McGuinness describes that the first memory section (532) has a first number of first areas (words) and the second memory section (534) has a second number of second areas, each of the first areas and the second areas has consecutive storage locations, each of the first number and the second number is equal to or larger than one (Col. 11, line 55-Col. 12, line 4).

10.   With regard to Claim 4, McGuinness discloses that the first number is equal to the second

number (Col. 11, line 55-Col. 12, line 4), as shown in Figure 8.

11.   With regard to Claim 6, McGuinness discloses that both the first number and the second

number can inherently be modified to equal any number (Col. 11, line 55-Col. 12, line 4), and

therefore the both the first number and the second number can have a value of one.

12.   With regard to Claim 7, McGuinness discloses that both the first number and the second

number can inherently be modified to equal any number (Col. 11, line 55-Col. 12, line 4), and

therefore the both the first number and the second number can have a value of two.

13.   With regard to Claim 8, McGuinness describes that each of the block units has m rows,

wherein m is an integer equal to or larger than four (Col. 10, lines 43-53).

14.   With regard to Claim 9, McGuinness describes that m is equal to thirty-two (Col. 10,

lines 43-53).

15.   With regard to Claim 10, Claim 10 is similar in scope to Claims 1 and 2, and therefore is

rejected under the same rationale. With regard to Claims 11 and 12, these claims are similar in

scope to Claims 3 and 8 respectively, and therefore are rejected under the same rationale.

16.   With regard to Claim 13, Claim 13 is similar in scope to Claim 1, except for the addition

of retrieving a prediction block of picture from the memory, retrieving the digital data

representing the prediction block stored in the first memory section, and retrieving the digital

data representing the prediction block stored in the second memory section. McGuinness

describes retrieving a prediction block of picture from the memory, retrieving the digital data

representing the prediction block stored in the first memory section (532, Figure 8), and

retrieving the digital data representing the prediction block stored in the second memory section

(534) (Col. 7, lines 64-67; Col. 11, line 51-Col. 12, line 32). Therefore, Claim 13 is rejected

under the same rationale as Claim 1.

17.   With regard to Claims 14 and 15, these claims are similar in scope to Claims 3 and 8

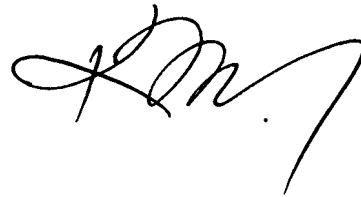respectively, and therefore are rejected under the same rationale.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Joni Hsu whose telephone number is 571-272-7785. The

examiner can normally be reached on M-F 8am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Kee Tung can be reached on 571-272-7794. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JH

KEE M. TUNG
SUPERVISORY PATENT EXAMINER